Selecting Coupled Inductors for SEPIC Applications



Application Design Example

The SEPIC (Single-Ended Primary Inductance Converter) topology is used in applications that require characteristics of both a buck and a boost regulator, specifically the ability to step up and step down the input voltage. Most often operated in CCM (Continuous Conduction Mode), SEPIC provides a non-inverted output voltage.

Typically, SEPIC is used in battery operated systems and automotive applications. In these applications, the battery input voltage, or bus line voltage, may be greater or less than that of the desired output voltage, depending on the charge state of the battery. The SEPIC topology can operate over more of the battery discharge cycle because of the ability to regulate the output voltage over a wider input voltage range, including above and below the output voltage.

A simplified SEPIC schematic, using coupled inductors is shown in Figure 1.



Figure 1. Simplified SEPIC Circuit Diagram

C1: Flying capacitor – Should be low ESR, most often ceramic.

C2: Output capacitor – Chosen based on output ripple current tolerances, usually tantalum.

L1 and L2: Coupled inductor – Two inductors closely coupled on a common core.

Inductors L1 and L2 do not have to be wound together on a single magnetic core, however there are distinct advantages if they are. If L1 and L2 are closely coupled, the ripple current is divided between them and the required inductance is halved. For example, two 22 μ H single inductors can be replaced with one coupled inductor of 11 μ H per winding, potentially allowing for the selection of a smaller inductor, or one the same size that has lower DCR and higher current handling. The selection of one coupled inductor over two single parts saves board space and can also save cost.

The following examples demonstrate the calculations necessary to identify the inductance, rms current and peak current ratings.

Coupled Inductor Calculation

Assume the following parameters for the circuit shown in Figure 1:

Target Efficiency = 90%

Step 1. Calculate minimum and maximum duty cycle $(D_{min} \text{ and } D_{max})$

$$D_{min} = \frac{V_{OUT} + V_D}{(V_{IN}(max) + V_{OUT} + V_D)}$$
$$D_{min} = \frac{3.3 V + 0.7 V}{(4.5 V + 3.3 V + 0.7 V)}$$

 $D_{min} = 0.47$

$$D_{max} = \frac{V_{OUT} + V_D}{(V_{IN}(min) + V_{OUT} + V_D)}$$
$$D_{max} = \frac{3.3 V + 0.7 V}{(2.7 V + 3.3 V + 0.7 V)}$$

 $D_{max} = 0.60$

Step 2. Calculate peak to peak ripple current

The allowed ripple current percentage must be chosen. A good starting point is to limit the peak to peak ripple current to 40% of the full load inductor Irms current. This ensures a low output ripple, minimizes core loss in the inductor and allows for light load continuous conduction mode operation to current as low as 20% of full load. Allowing for 40% ripple, calculate the worst case peak to peak ripple current at $V_{IN}(min)$ using the following equation:

$$I_{ripple} = I_{OUT} \times \frac{V_{OUT}}{V_{IN}(min)} \times (\% \text{ peak-peak ripple current})$$

$$I_{ripple} = I_{OUT} \times \frac{V_{OUT}}{V_{OUT}} \times 0.4$$

$$Iripple = I_{OUT} \times \frac{1}{V_{IN}(min)} \times 0.4$$
$$Iripple = 0.2 \text{ A} \times \frac{3.3 \text{ V}}{27 \text{ V}} \times 0.4$$

Iripple = 0.098 A

Step 3. Calculate inductance

Inductance is calculated by the fundamental equation:

$$V = L \times \frac{\triangle i}{\triangle t}$$

Because the two windings of a coupled inductor share the ripple current, the inductance value can be halved.

$$L1 = L2 = 0.5 \times \frac{V_{IN}(min)}{I_{ripple} \times F_S} \times D_{max}$$
$$L1 = L2 = 0.5 \times \frac{2.7 V}{0.098 \text{ A} \times 400 \text{ kHz}} \times 0.60$$

L1 = L2 = 20.7 μH

Choose standard value L = 22 μ H

Step 4. Calculate required Irms

L1 Irms =
$$\frac{V_{OUT} \times I_{OUT}}{V_{IN} (min) \times Efficiency}$$

L1 Irms =
$$\frac{3.3 V \times 0.2 A}{2.7 V \times 0.9}$$

L1 Irms = 0.27 A

 $L2 Irms = I_{OUT}$ L2 Irms = 0.2 A

Step 5. Calculate Ipeak

L1
$$I_{peak} = I_{rms} + (0.5 \times I_{ripple})$$

L1 $I_{peak} = 0.27 A + (0.5 \times 0.098 A)$
L1 $I_{peak} = 0.32 A$

L2
$$I_{peak} = I_{rms} + (0.5 \times I_{ripple})$$

$$L2 I_{peak} = 0.2 A + (0.5 \times 0.098 A)$$

L2 Ipeak = 0.25 A

Step 6. Summarize inductor specifications

L1 = L2 = 22 µH

 $I_{rms} (L1) = 0.27 \text{ A} \qquad I_{peak} (L1) = 0.32 \text{ A} \\ I_{rms} (L2) = 0.20 \text{ A} \qquad I_{peak} (L2) = 0.25 \text{ A}$

Step 7. Select the coupled inductor

Choose Coilcraft LPD4012-223ML.

Coilcraft LPD4012-223ML has 22 μ H per winding, and a saturation rating of 0.70 A. This means that 0.35 A per winding (or any combination that does not exceed 0.70 A) can flow without saturation.

Coilcraft LPD4012-223ML has Irms rating of 0.34 A for both windings and 0.48 A for a single winding. This means that for a 40°C temperature rise, up to 0.34 A can flow in each winding simultaneously or up to 0.48 A can flow in one winding.

For this example, I_{rms} of L1 (0.27 A) and I_{rms} of L2 (0.20 A) are well below these limits. Refer to the LPD4012 data sheet, and use the following formulas to calculate the temperature rise (Δt):

Power loss = $(I_{L1}^2 + I_{L2}^2) \times DCR$

Temperature rise (Δt) = Power loss × $\frac{135^{\circ}C}{W}$ Δt = power loss × $\frac{135^{\circ}C}{W}$

Power loss = $(0.27^2 + 0.20^2) \times 1.52 = 0.172 W$

$$\Delta t = 0.172 \ W \times \frac{135^{\circ}C}{W} = 23^{\circ}C$$

Step 8. Frequency Dependent Losses

In most cases the I²R loss calculated in Step 7 provides a good estimate of the inductor losses. However, frequency dependent losses such as core loss and ac winding loss should also be considered.

To estimate total inductor loss including ac winding resistance and core loss refer to the Coilcraft Coupled Inductor Finder and Analyzer tool at www.coilcraft.com/en-us/tools/coupled-inductor-finder/#/search

Calculate the Total inductor loss for each winding. To determine temperature rise, add the Total inductor loss for each winding together, and multiply this number by 135 as shown in step 7.

References

Designing a SEPIC Converter, Wei Gu, National Semiconductor Application Note AN1484, June 2007

Versatile Low Power SEPIC Converter Accepts Wide Input Voltage Range, Jack Palczynski, Unitrode Design Note DN-48.

SEPIC Equations and Component Ratings, Maxim In-